

**Remarks**

Applicant would like to thank the Examiner and the Primary for the courteous interview extended to Applicant's representative on Monday, January 3, 2005. During the interview, the objection to the drawings in paragraph 5 of the present office action was withdrawn by the Examiner after reconsideration of the objection in view of Figure 7. Additionally, during the interview the Official Notice was discussed, and Applicant herein presents no arguments with respect to the Official Notice taken by the Examiner in the Office Action of September 14, 2004.

Pursuant to the substantive discussion during the interview surrounding the Kwon and Lofgren references, Applicant has amended the independent claims to more clearly define the invention. Dependent claims have been amended to correct antecedent bases due to the amendments to the independent claims. After the foregoing amendment, claims 44 – 63 are pending, with claims 44, 56, and 61 being the independent claims.

***35 USC §103(a) Kwon in view of Lofgren***

As discussed during the interview, the Kwon reference is deficient because it does not teach or disclose a serially addressed memory that is directly connected to the processing unit via a serial address and data line. In fact, Kwon teaches that the memory is indirectly connected to the processing unit via parallel lines. Similarly, Lofgren is also deficient in this area and teaches only a parallel bus architecture connecting the processing unit to the mass storage memory device.

The independent claims have been amended to clarify this aspect of the invention and accordingly, neither Kwon nor Lofgren alone or in combination teach the claimed invention. In fact, both Kwon and Lofgren are directed to conventional configurations described by Applicant in the background section. For example, Applicant points out in the background that the use of parallel memory is by far the dominant configuration in the cell phone market (see paragraphs 8 – 10 for the discussion).

Furthermore, because of the dominant use and specific advantages of using parallel memory in a cell phone device, one having skilled in the art would not be motivated to connect a serial memory to a processor by a serial address and data line. In fact, one having skill in the art would be motivated NOT to connect a serial memory to a processor

by a serial address and data line because of the inherent disadvantages of such a configuration in the marketplace.

Accordingly, Applicant asserts that the claims are presently in condition for allowance over the prior art of record and any potential new prior art references will lack any teachings or suggestions for a combination that teaches the claimed invention because of the overwhelming motivation for one having skill in the art to NOT make such a combination.

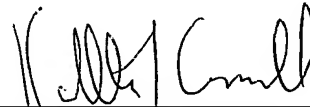
**Conclusion**

If the Examiner has any questions or comments regarding the above Amendments and Remarks, the Examiner is respectfully urged to contact the undersigned at the number listed below.

Respectfully submitted,

Dated: January 14, 2005

By: \_\_\_\_\_

  
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